

VISIBLE LIGHT PRODUCTS SPECIFICATION

HCL-1909AWE-1



Drawn by	Checked by	Approved by



DATE:2011/9/7

REV:I



HUEY JANN ELECTRONICS INDUSTRY CO., LTD.

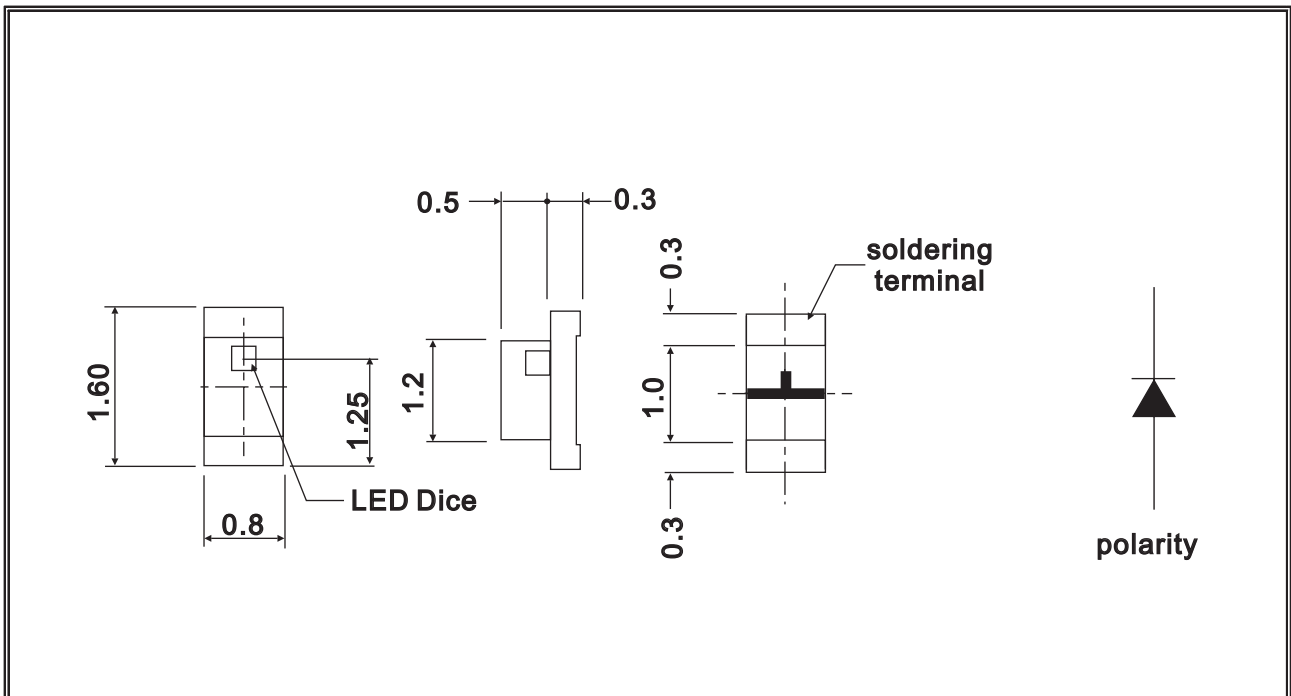
No.27 Line 466 Sec.2,Cannng-nan Rd. Wu-chi Town Taichung Shien, Taiwan, R.O.C.

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DEVICES

Part Number	Lens		Source	
	Color	Diffusion	Dice Source	Color
HCL-1909AWE-1	Yellow	Diffusion	GaNN/GaN	Super White

PACKAGE DIMENSIONS



NOTE:

- 1.All dimensions are in millimeter.
- 2.specifications are subject to change without notice.
- 3.Tolerance is 0.3mm unless otherwise noted.

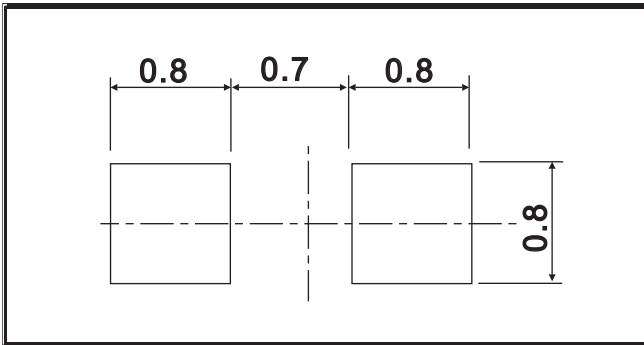


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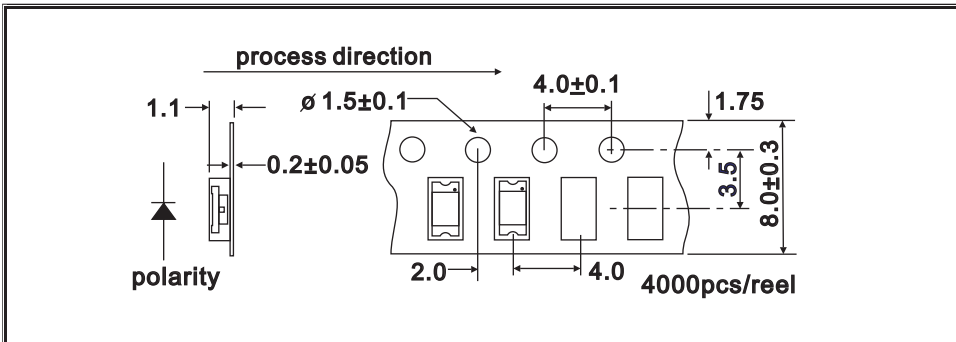
RECOMMENDED SOLDERING PATTERN



NOTE:

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TAPE SPECIFICATIONS



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ABSOLUTE MAXIMUM RATINGS

TA=25°C

PARAMETER	SYMBOL	MAX. RATING	UNIT
Power Dissipation	Pd	120	mW
Continuous Forward Current	IF	20	mA
Peak Forward Current *1	IFM	100	mA
Reverse Voltage	VR	4	V
Electrostatic Discharge (HBM)	ESD	150	V
Operating Temperature	Topr	-40 ~ +85	°C
Storage Temperature	Tstg	-40 ~ +85	°C
Reflow Soldering (preheat 150-180°C 60-120sec, soldering temp 260°C 10sec)			

*1.Duty Ratio=0.1%,Pulse Width=10us.

*2.Iron soldering in 350°C within 5 seconds will not cause damage to the dice. But be aware of the high temperature will not only make the epoxy soften but also cause the lead moving and the gold wire broken and even open. So before returning to the normal temperature PLEASE AVOID any serious pressure on the top of epoxy and lead.



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ELECTRIC-OPTICAL CHARACTERISTICS

TA=25°C

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
View Angle of Half Power	2θ1/2	IF=20mA		140		deg
Forward Voltage	VF			3.3	3.8	V
Reverse Current	IR	VR=4V			60	μA
Luminous Intensity *2	IV	IF=20mA	200	400		mcd
Chromaticity Coordinates	x			0.29		
	y			0.31		
Spectrum Width Of Half Valve	Δλ				---	

*2.Tolerance: 15% HUEY-JANN measuring equipment : EXELTRON 2001. 2.S370 made by U.D.T.

*3.The dominate wavelength , λ d, is derived from the CIE Chromaticity Diagram and represents the color of the device.



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RELIABILITY TEST

Classification	Test Item	Reference Standard	Test Conditions	Result
Endurance Test	Operation Life	MIL-STD-750:1026 MIL-STD-883:1005 JIS-C-7021 :B-1	Connect with a power if=20mA Ta=Under room temperature Test Time=1,000hrs	0/22
	High Temperature High Humidity Storage	MIL-STD-202:103B JIS-C-7021 :B-11	Ta=+85°C 5°C RH=90% ~ 95% Test Time=1000hrs	0/22
	High Temperature Storage	MIL-STD-883:1008 JIS-C-7021 :B-10	High Ta=+85°C 5°C Test Time=1,000hrs	0/22
	Low Temperature Storage	JIS-C-7021 :B-12	Low Ta=-40°C 5°C Test Time=1,000hrs	0/22
	Environmental Test	Temperature Cycling	MIL-STD-202:107D MIL-STD-750:1051 MIL-STD-883:1010 JIS-C-7021 :A-4	-40°C ~ +25°C ~ +85°C ~ +25°C 60min 20min 60min 20min Test Time=200cycle
Thermal Shock		MIL-STD-202:107D MIL-STD-750:1051 MIL-STD-883:1010	-40°C 5°C ~ +85°C 5°C 20min 20min Test Time=200cycle	0/22

*Failure Criteria:

1. VF arise $\geq 10\%$
2. IV decline $\geq 30\%$
3. A failure is an LED that is open or shorted

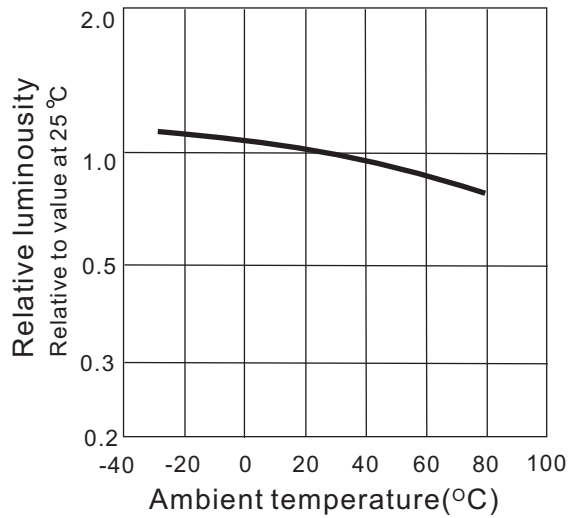
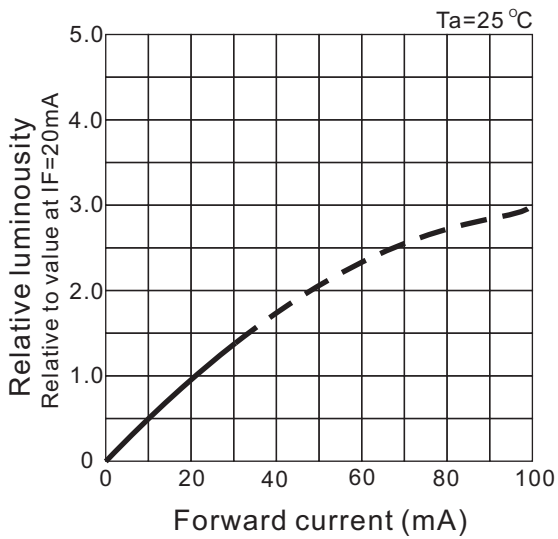
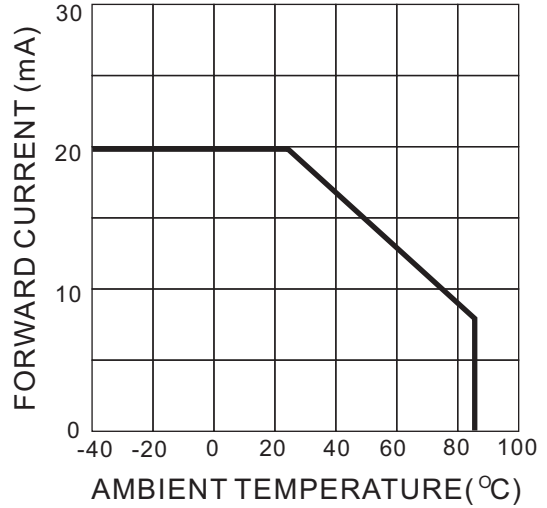
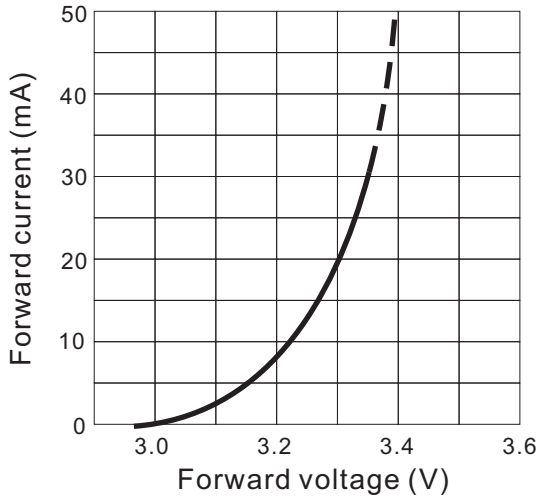


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TYPICAL ELECTRICAL OPTICAL CHARACTERISTICS CURVES

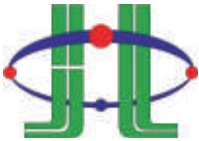
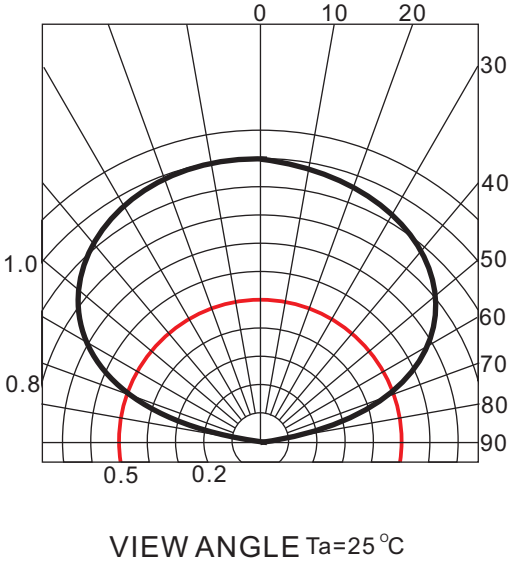
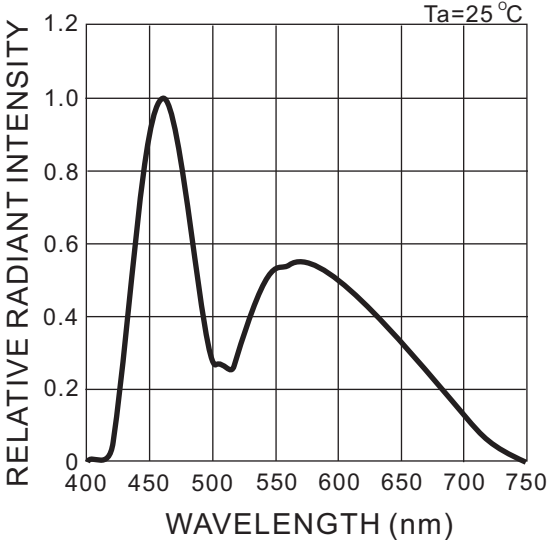


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LED VF Bin Selection

$I_F=20\text{mA}$

BIN CODE	Forward Voltage	
	Minimum	Maximum
I	2.8	3.0
j	3.0	3.2

Voltage tolerance for each bin limit is $\pm 0.03\text{V}$

Brightness Bin Selection

$I_F=20\text{mA}$

BIN CODE	Brightness in mcd	
	Minimum	Maximum
K	310	400
L	400	520
M	520	680

Brightness tolerance for each bin limit is $\pm 15\%$



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Color Bin Selection

IF=20mA

Bin		Chromaticity Coordinates				
3	3-1	x	0.260	0.270	0.270	0.260
		y	0.280	0.290	0.270	0.260
	3-2	x	0.260	0.270	0.270	0.260
		y	0.260	0.270	0.240	0.230
4	4-1	x	0.270	0.280	0.280	0.270
		y	0.290	0.300	0.280	0.270
	4-2	x	0.270	0.280	0.280	0.270
		y	0.270	0.280	0.250	0.240
5	5-1	x	0.280	0.290	0.290	0.280
		y	0.300	0.310	0.290	0.280
	5-2	x	0.280	0.290	0.290	0.280
		y	0.280	0.290	0.260	0.250
6	6-1	x	0.290	0.300	0.300	0.250
		y	0.310	0.320	0.300	0.290
	6-2	x	0.250	0.300	0.300	0.290
		y	0.290	0.300	0.270	0.260
7	7-1	x	0.300	0.310	0.310	0.300
		y	0.320	0.330	0.310	0.300
	7-2	x	0.300	0.310	0.310	0.300
		y	0.300	0.310	0.280	0.270
Tolerance			$x \pm 0.02$	$y \pm 0.02$		

Bin		Chromaticity Coordinates				
8	8-1	x	0.310	0.320	0.320	0.310
		y	0.330	0.340	0.320	0.310
	8-2	x	0.310	0.320	0.320	0.310
		y	0.310	0.320	0.290	0.280
9	9-1	x	0.320	0.330	0.330	0.320
		y	0.360	0.370	0.350	0.340
	9-2	x	0.320	0.330	0.330	0.320
		y	0.340	0.350	0.315	0.305
10	10-1	x	0.330	0.340	0.340	0.330
		y	0.370	0.380	0.360	0.350
	10-2	x	0.330	0.340	0.340	0.330
		y	0.350	0.360	0.325	0.315
11	11-1	x	0.340	0.350	0.350	0.340
		y	0.380	0.390	0.370	0.360
	11-2	x	0.340	0.350	0.350	0.340
		y	0.360	0.370	0.335	0.325
12	12-1	x	0.350	0.360	0.360	0.350
		y	0.390	0.400	0.380	0.370
	12-2	x	0.350	0.360	0.360	0.350
		y	0.370	0.380	0.345	0.335
Tolerance			$x \pm 0.02$	$y \pm 0.02$		

The chromaticity coordinates reading at center of light.

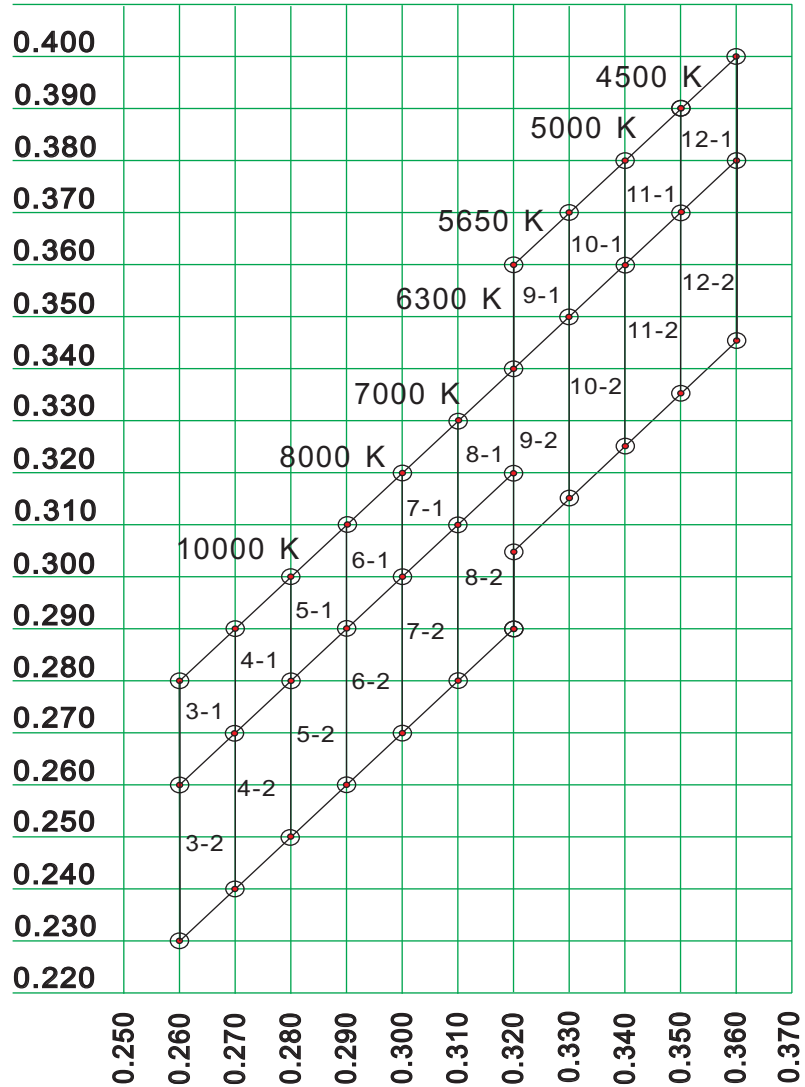


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CIE Light Color Chart



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Precautions For Use

1. Over-current-proof

Customer must apply resistors for protection , otherwise slight voltage shift will cause big current change (Burn out will happen).

2. Storage

2.1 Do not open moisture proof bag before the products are ready to use.

2.2 Before opening the package, the LEDs should be kept at 30°C or less and 90%RH or less.

2.3 The LEDs should be used within a year.

2.4 After opening the package, the LEDs should be kept at 30°C or less and 60%RH or less.

2.5 The LEDs should be used within 168 hours (7 days) after opening the package.

2.6 If the moisture absorbent material (silica gel) has faded away or the LEDs have exceeded the storage time, baking treatment should be performed using the following conditions. Baking treatment : 60 5°C for 24 hours.

3. Soldering Condition

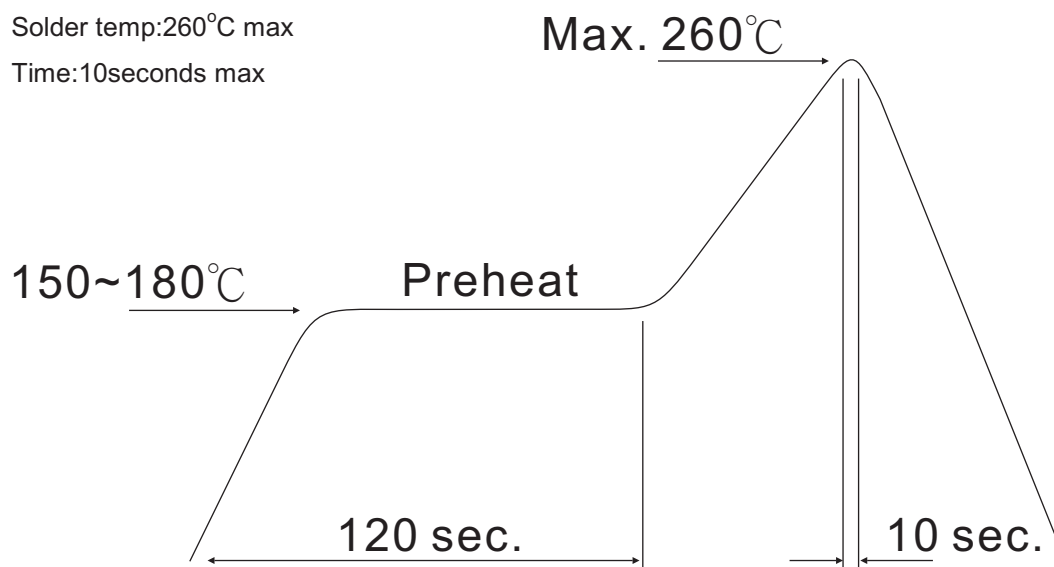
3.1 Pb-free solder temperature profile

Preheat:150 ~ 180°C

60 ~ 120seconds

Solder temp:260°C max

Time:10seconds max



3.2 Reflow soldering should not be done more than two times.

3.3 When soldering, do not put stress on the LEDs during heating.

3.4 After soldering, do not warp the circuit board.



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