

3057

T-74-05-01

CMOS LSI

Graphic Equalizer's Spectrum Analyzing Display LCD Driver

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Use

- Graphic equalizer's spectrum analyzing display LCD driver

Features

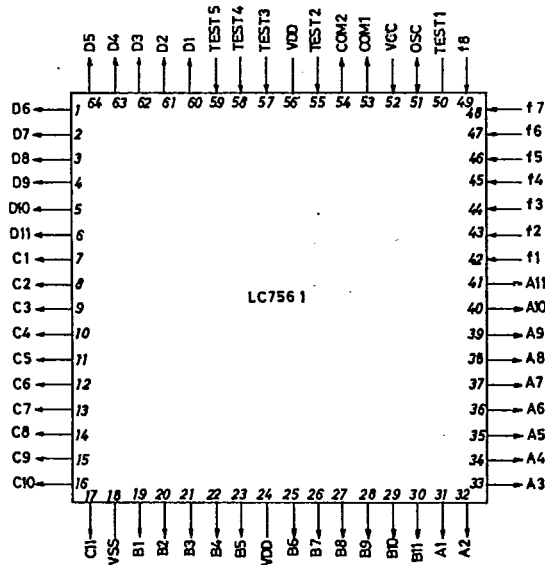
- (1) 8-band display: 11 x 8 segments
(Display of bandpass signal strength → Spectrum analyzing display: 8 bands 2dB/step, 11-dot display)
- (2) The signal strength display input is the L/R mixing input.
(The mixing circuit is connected externally.)
- (3) Display unit: Dynamic drive of LCD. 1/2duty, 1/2bias (5V rating)
- (4) Bandpass filter for display: External equivalent filter

Absolute Maximum Ratings at Ta=25°C, VSS=0V

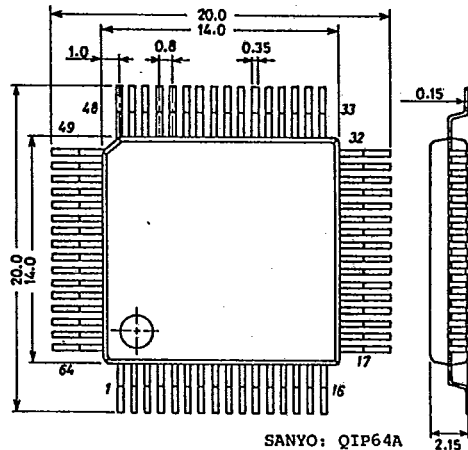
Parameter	Symbol	Condition	Unit
Maximum Supply Voltage	V _{DDmax}	V _{DD}	V
	V _{CCmax}	V _{CC}	V
Maximum Input Voltage	V _{I1max}	TEST2, TEST3	V
	V _{I2max}	f1tof8, TEST1, TEST4, TEST5	V
Maximum Output Voltage	V _{Omax}	A1toA11, B1toB11, COM1, COM2, C1toC11, D1toD11, COM2	V
Allowable Power Dissipation	P _{dmax}	Ta ≤ 75°C	200 mW
Operating Temperature	Topg		-30 to +75 °C
Storage Temperature	Tstg		-40 to +125 °C



Pin Assignment



Case Outline 3057-Q64AIC (unit:mm)



D227AT/6066AT, TS No.2014-1/4

LC7561

T-74-05-01

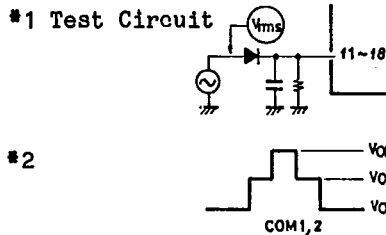
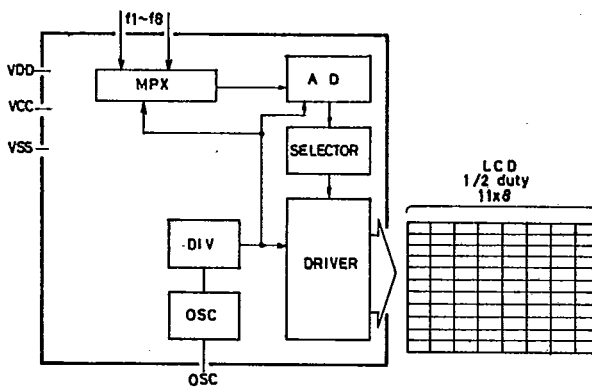
Allowable Operating Conditions at Ta=25°C, V_{SS}=0V

	V _{DD}	V _{DD}	C of 0.1uF or greater must be connected.	min	typ	max	unit
Supply Voltage	V _{DD}	V _{DD}		7.5	13.0	14.0	V
External CR	V _{CC}	V _{CC}		4.0	5.0	5.5	V
	Rosc	OSC			75		kohm
	Cosc	OSC		0.0033			uF

Electrical Characteristics at Ta=25°C, V_{SS}=0V

			min	typ	max	unit
Input Sensitivity	V _{in}	f1to8:V _{DD} =13V, OdB, lighted, Test Circuit #1		1.6		V
A/D Conversion Error	ΔB	f1to8:V _{DD} =13V, to 2dB step, Test Circuit #1	-1		1	dB
Current Dissipation	I _{DD}	V _{DD}			7	mA
Input OFF-State Leak Current	I _{CC}	V _{CC}			1	mA
	I _{off}	f1to8			10	uA
Output 'H'-Level Voltage	V _{OH}	A1to11, B1to11, COM1, C1to11, D1to11, COM2	0.8V _{CC}		V _{CC}	V
Output 'L'-Level Voltage	V _{OL}	A1to11, B1to11, COM1, C1to11, D1to11, COM2	V _{SS}		0.2V _{CC}	V
Output 'M'-Level Voltage	V _{OM}	COM1, 2 #2		1/2V _{CC}		V
Output Impedance	Z _O	A1to11, B1to11, COM1, C1to11, D1to11, COM2		10		kohm

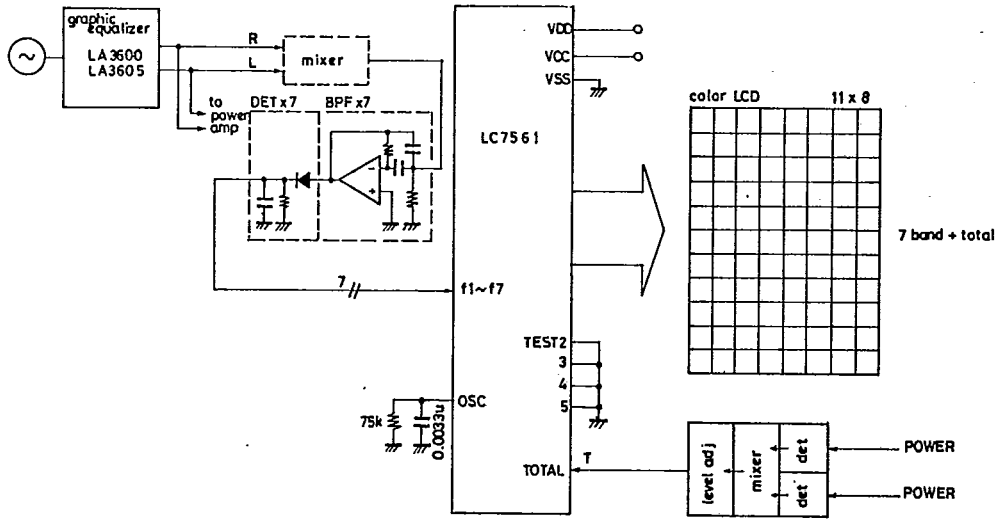
Equivalent Circuit Block Diagram



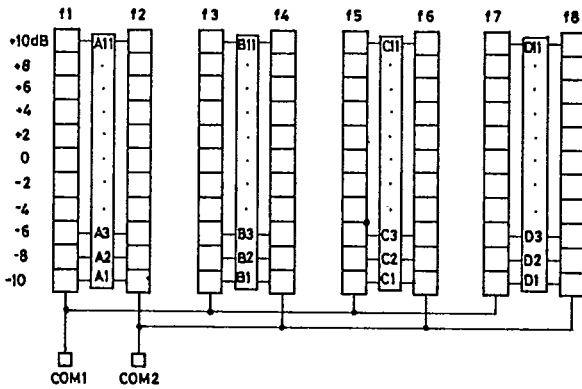
LC7561

T-74-05-01

Sample Application Circuit
7-band mixing display + total display

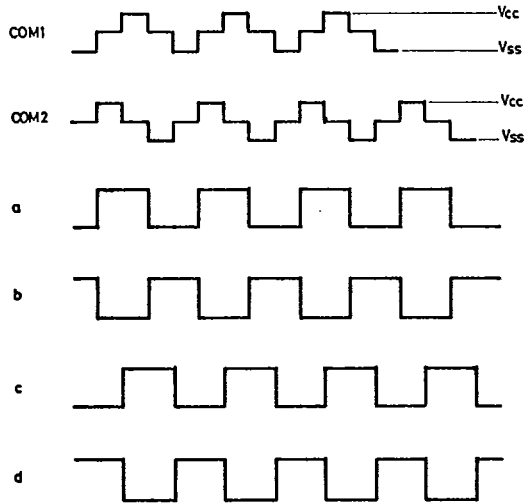


Segment Assignment



COM1,2,A1 to A11,B1 to B11,C1 to C11,D1 to D11: Pin name of LC7561

The waveforms at COM1, COM2 are as shown below. One of the four modes of a to d is selected by the selector inside the LC7561 according to the lighting state and is delivered at the segments.



	COM1	COM2
a	X	X
b	○	○
c	X	○
d	○	X

○ : Lighted
X : Unlighted

Pin Description

Pin Name	Pin No.	Pin Configuration	Description
V _{DD}	25,56		. Power supply pin, +13V typ., power supply for A/D conversion.
V _{CC}	52		. Power supply pin, +5V, power supply for logic drive.
V _{SS}	18		. Power supply pin, 0V.
COM1 COM2	53 54		. Output pins to LCD common.
A1 to 11	31 to 41		. Output pins to LCD segments. . For bands f1, f2
B1 to 11	19 to 23 25 to 30		. Output pins to LCD segments. . For bands f3, f4
C1 to 11	7 to 17		. Output pins to LCD segments. . For bands f5, f6
D1 to 11	60 to 64 1 to 6		. Output pins to LCD segments. . Band f7, total display or accessory display.
f1 to 8	42 to 49		. Used to input detection output of audio signal.
OSC	51		. Open drain type output buffer. . Used to connect external CR for OSC.
TEST4	58		. IC test pin. . Connected to V _{DD} or V _{SS} in normal use.
TEST5	59		
TEST2	55		. IC test pin. . Connected to V _{CC} or V _{SS} in normal use.
TEST3	57		
TEST1	50		. IC test pin. . Open in normal use.

T-90-20

AUDIO-USE MOS IC CASE OUTLINES

- All of Sanyo audio-use MOS IC case outlines are illustrated below.
- All dimensions are in mm, and dimensions which are not followed by min. or max. are represented by typical values.
- No marking is indicated.

