

Vishay Siliconix

P-Channel 60-V (D-S) MOSFET

FEATURES

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TrenchFET[®] Power MOSFET

www.vishay.com/doc?99912

For definitions of compliance please see

100 % UIS Tested

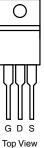
APPLICATIONS

· Load Switch

Material categorization:

PRODU	CT SUMMARY		
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
- 60	0.0195 at V _{GS} = - 10 V	- 53	76 nC
- 00	0.0250 at V _{GS} = - 4.5 V	- 42	70110

TO-220AB



DRAIN connected to TAB

Ordering Information: SUP53P06-20-E3 (Lead (Pb)-free) SUP53P06-20-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATING	$\mathbf{J}(1_{A} = 25 \text{ O}, \text{united})$				
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 60	V		
Gate-Source Voltage		V _{GS}		± 20	
	T _C = 25 °C		- 53 ^a	_	
Continuous Drain Current (T 150 °C)	T _C = 70 °C		- 46.8		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	9.2 ^b		
	T _A = 70 °C		- 8.1 ^b	A	
Pulsed Drain Current	I _{DM}	- 150			
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	- 45		
Single Pulse Avalanche Energy	L = 0.1 IIIA	E _{AS}	101	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C		69 ^a	A	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	2.1 ^b	A	
	T _C = 25 °C		104.2 ^a		
Maximum Power Dissipation	T _C = 70 °C	Р	66.7 ^a	w	
	T _A = 25 °C	P _D	3.1 ^b	VV	
	T _A = 70 °C		2 ^b		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^b	Steady State	R _{thJA}	33	40	°C/W
Maximum Junction-to-Case	Steady State	R _{thJC}	0.98	1.2	0/1

Notes:

a. Based on T_C = 25 °C.

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b. Surface mounted on 1" x 1" FR4 board.

For technical questions, contact: pmostechsupport@vishav.com

RoHS COMPLIANT HALOGEN FREE



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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -250 \mu A$	- 60			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μΑ		68		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	i _D = - 230 μA		- 5.2		mv/ C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zara Cata Valtaga Drain Current	1	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	
Zero Gate Voltage Drain Current	IDSS	V_{DS} = - 60 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10	μA
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 120			А
	Р	V _{GS} = - 10 V, I _D = - 30 A		0.0160	0.0195	0
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 20 A		0.0200	0.0250	Ω
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 50 A	20			S
Dynamic ^b				•		
Input Capacitance	C _{iss}			3500		
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		390		pF
Reverse Transfer Capacitance	C _{rss}		290			1
Tatal Cata Charge	0	$V_{DS} = -30$ V, $V_{GS} = -10$ V, $I_{D} = -55$ A		76	115	
Total Gate Charge	Qg			38	60	
Gate-Source Charge	Q _{gs}	V_{DS} = - 30 V, V_{GS} = - 4.5 V, I_{D} = - 55 A		16		nC
Gate-Drain Charge	Q _{gd}			19		
Gate Resistance	Rg	f = 1 MHz		5.2		Ω
Turn-On Delay Time	t _{d(on)}			10	15	
Rise Time	t _r	V_{DD} = - 2 V, R_L = 2 Ω		7	15	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 10 A, V_{GEN} = - 10 V, R_g = 1 Ω		70	110	- ns
Fall Time	t _f			40	60	
Drain-Source Body Diode Characteristic	s			•		
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 69	•
Pulse Diode Forward Current ^a	I _{SM}				- 150	A
Body Diode Voltage	V _{SD}	I _S = - 30 A		- 1	- 1.5	V
Body Diode Reverse Recovery Time	t _{rr}			45	68	ns
Body Diode Reverse Recovery Charge	Q _{rr}	$L = 50 \text{ A di/dt} = 100 \text{ A/ve} \text{ T} = 05 \text{ e}^{\circ}$		59	120	nC
Reverse Recovery Fall Time	ta			29		
Reverse Recovery Rise Time	t _b			16		ns

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

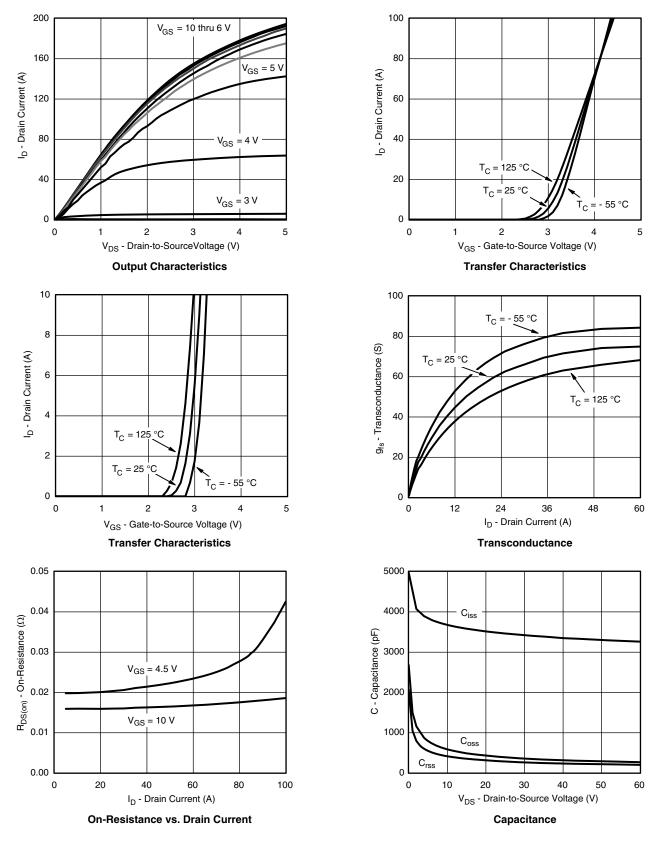
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



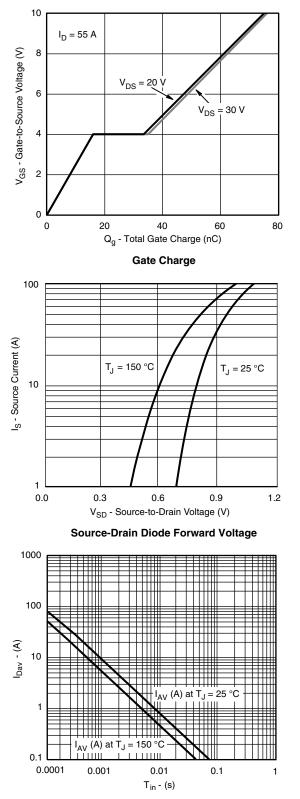
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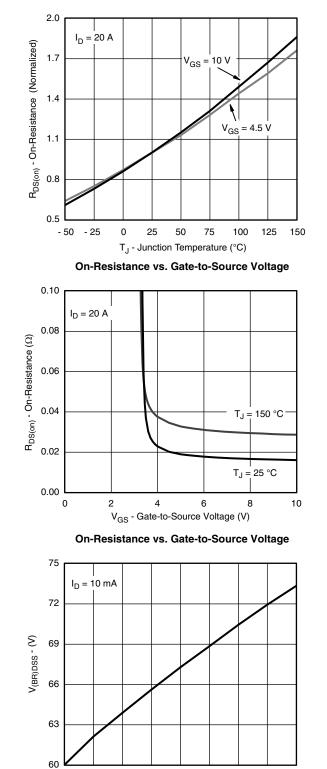
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Single Pulse Avalanche Current Capability vs. Time



- 50 - 25 0 25 50 75 100 125 150 T_J - Temperature (°C)

Drain-Source Breakdown Voltage vs. Junction Temperature

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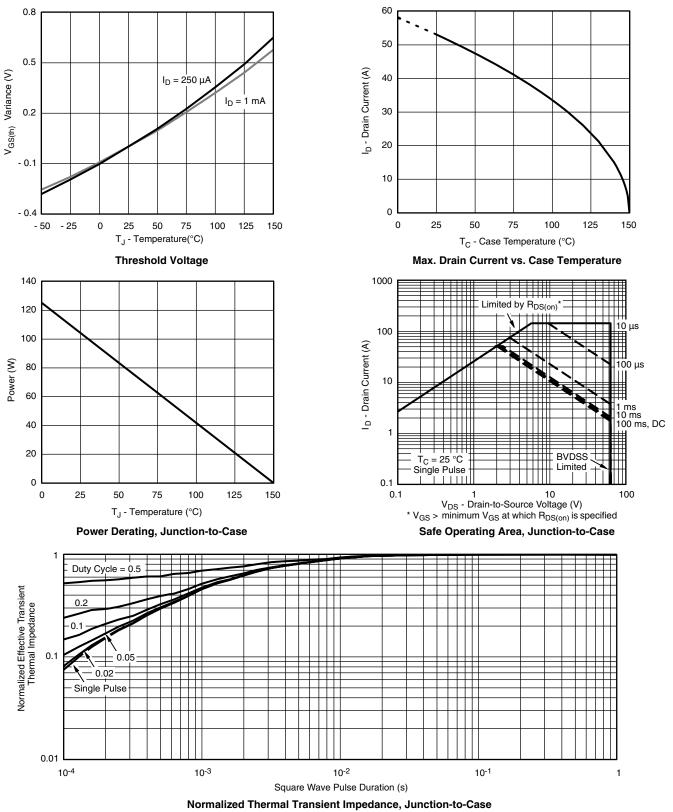
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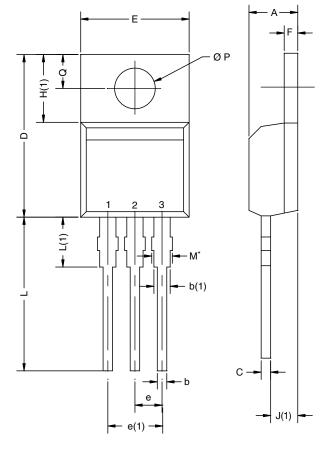
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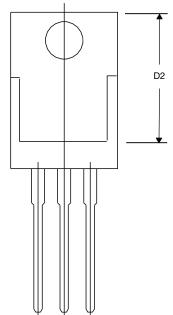
TO-220AB



	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
D2	12.19	12.70	0.480	0.500	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØР	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: T14-0 DWG: 5471	0413-Rev. P, 1	16-Jun-14	•	•	

Note

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



Revison: 16-Jun-14

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